



RocketRAID 3500 Series GNU GPL Code Accepted into Linux Kernel

- RocketRAID 3500 Series GNU GPL code officially recognized and accepted into Linux kernel 2.6.25
- All RocketRAID 3500 Series hardware included into Linux kernel 2.6.25

[ChangeLog.svm53c8xx_2](#)
[fc395x.txt](#)
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[fc3x80.txt](#)
[FlashPoint.txt](#)
[NCR5380.txt](#)
[hptiop.txt](#)
[bmmca.txt](#)
[n2000.txt](#)
[ibsas.txt](#)
[LICENSE.FlashPoint](#)
[LICENSE.qta2xxx](#)
[ink_power_management_policy.txt](#)
[pfc.txt](#)
[megaraid.txt](#)
[Mylex.txt](#)
[r53c8xx.txt](#)
[NinjaSCSI.txt](#)
[psst.txt](#)
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[logicfas.txt](#)
[scsi-changer.txt](#)
[scsi-generic.txt](#)
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[scsi_ah.txt](#)
[scsi_fc_transport.txt](#)
[scsi_mid_low_api.txt](#)
[st.txt](#)
[svm53c500_cs.txt](#)
[svm53c8xx_2.txt](#)
[mscsim.txt](#)

[Linux Kernel Development](#)
Kernel Device Drivers,
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Development
[www.breakt.co.il](#)

```
1 HIGHPOINT ROCKETRAID 3xxx/4xxx ADAPTER DRIVER (hptiop)
2
3 Controller Register Map
4 -----
5
6 For Intel IOP based adapters, the controller IOP is accessed via PCI BAR0:
7
8     BAR0 offset  Register
9             0x10  Inbound Message Register 0
10            0x14  Inbound Message Register 1
11            0x18  Outbound Message Register 0
12            0x1C  Outbound Message Register 1
13            0x20  Inbound Doorbell Register
14            0x24  Inbound Interrupt Status Register
15            0x28  Inbound Interrupt Mask Register
16            0x30  Outbound Interrupt Status Register
17            0x34  Outbound Interrupt Mask Register
18            0x40  Inbound Queue Port
19            0x44  Outbound Queue Port
20
21 For Marvell IOP based adapters, the IOP is accessed via PCI BAR0 and BAR1:
22
23     BAR0 offset  Register
24            0x20400  Inbound Doorbell Register
25            0x20404  Inbound Interrupt Mask Register
26            0x20408  Outbound Doorbell Register
27            0x2040C  Outbound Interrupt Mask Register
28
29     BAR1 offset  Register
30            0x0     Inbound Queue Head Pointer
31            0x4     Inbound Queue Tail Pointer
32            0x8     Outbound Queue Head Pointer
33            0xC     Outbound Queue Tail Pointer
34            0x10    Inbound Message Register
35            0x14    Outbound Message Register
36            0x40-0x1040  Inbound Queue
37            0x1040-0x2040  Outbound Queue
38
39
40 I/O Request Workflow
41 -----
42
```